

Notice of References Cited	Application/Control No. 10/621,067	Applicant(s)/Patent Under Reexamination FARKAS ET AL.	
	Examiner KENNETH TANG	Art Unit 2195	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2004/0111708	06-2004	Calder et al.	717/131
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Paker et al., "A heterogeneous multi-core platform for low power signal processing in systems-on-chip", Sept. 24-26, 2002, Solid-State Circuits Conference, 2002. ESSCIRC 2002. Proceedings of the 28th European, pages 73-76.
	V	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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